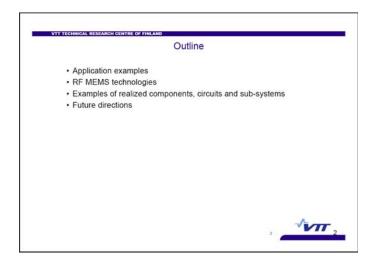
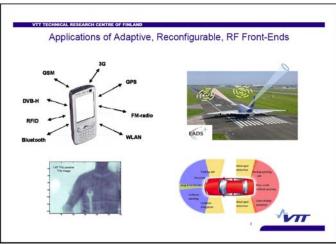
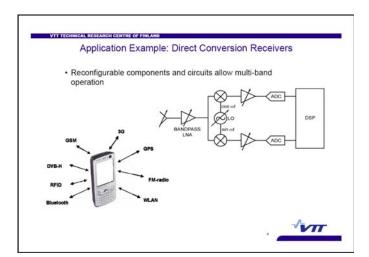
RECONFIGURABLE RF MEMS CIRCUITS

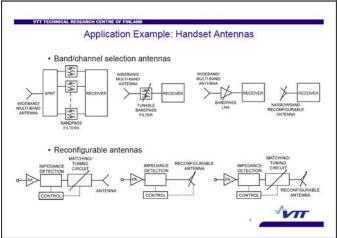
Tauno Vähä-Heikkilä

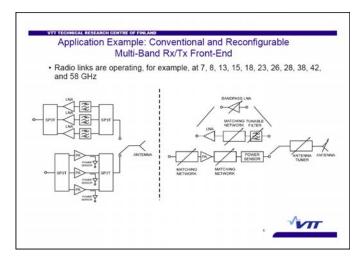
MilliLab, VTT Technical Research Centre of Finland Espoo, Finland

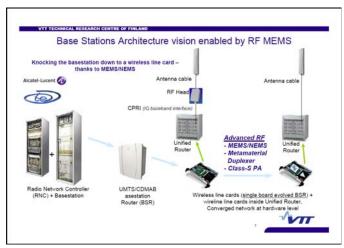


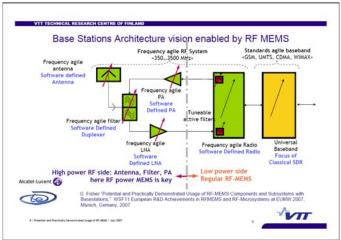




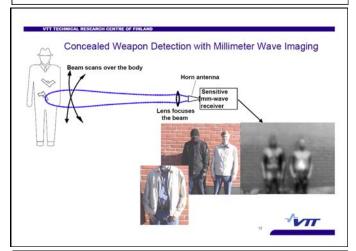


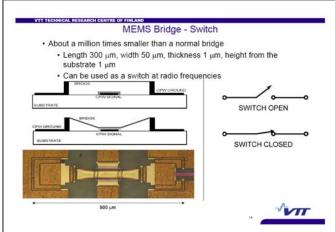


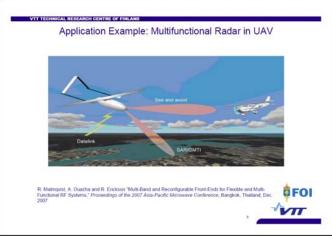




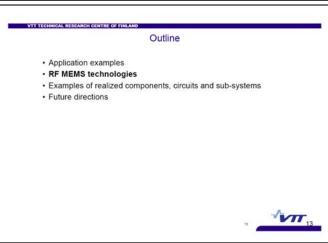


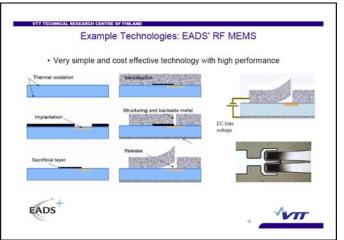


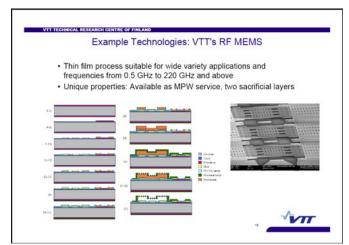


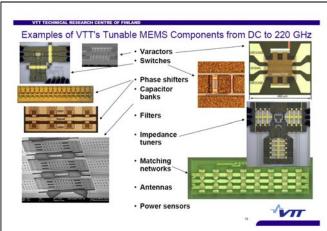


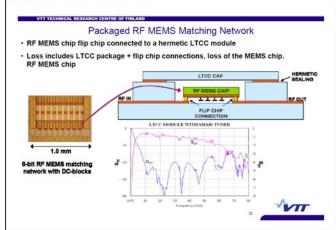


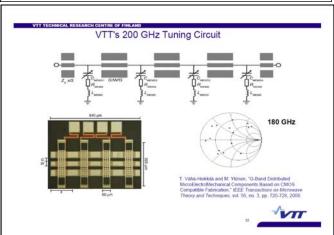




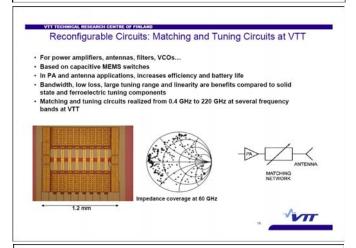


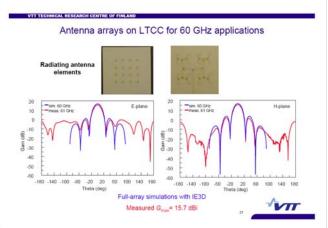


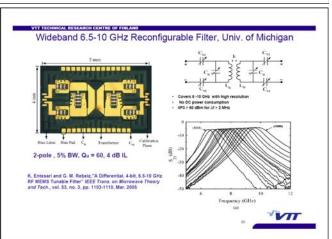


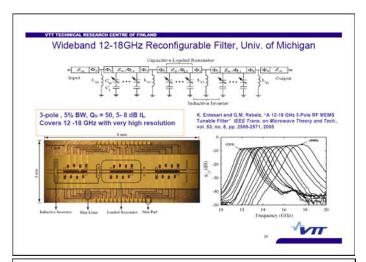


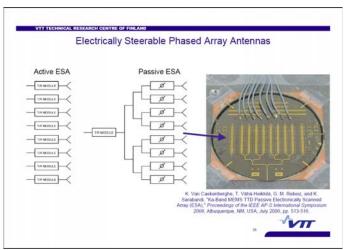
Outline Application examples RF MEMS technologies Examples of realized components, circuits and sub-systems Future directions

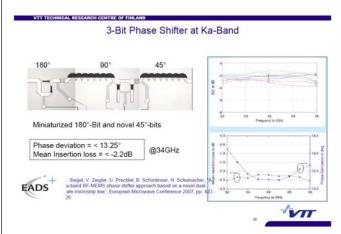


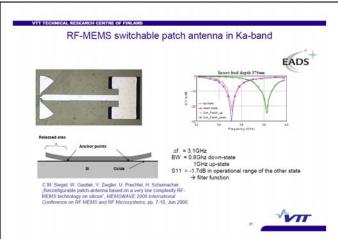


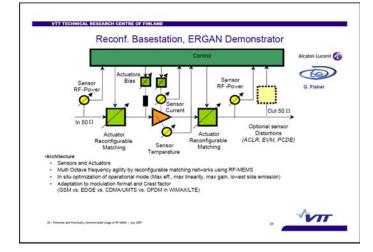


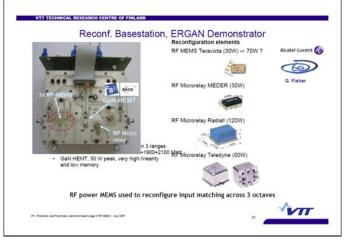


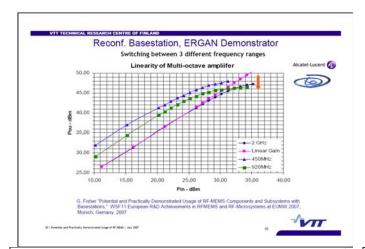












VTT TECHNICAL RESEARCH CENTRE OF FINLAND

Outline

- · Application examples
- · RF MEMS technologies
- · Examples of realized components, circuits and sub-systems
- · Future directions



Technologies Available for Everybody RF-PLATFORM MPW Run Schedule

· LTCC

- Deadline for designs: Jan. 28th 2009. Technology provider: VIA
- · SiGe2RF
- Deadline for designs: Dec. 15th 2008. Technology provider: ATMEL · RF MEMS - 1
- - Deadline for designs: Dec. 18th 2008. Technology provider: Fraunhofer ISIT
- RF MEMS 2
- Deadline for designs: Dec. 12th 2008. Technology provider: THALES
- IPD (Integrated Passive Devices) -1 copper layer
 Deadline for designs: Jan. 16th 2009. Technology provider: VTT
- IPD -2 copper layers
 - Deadline for designs: Oct 31st 2008. Technology provider: VTT





Future Directions (1)

- · Passive concepts
 - Passive elements such as antennas, feed networks, matching circuits are fabricated to low loss substrates with RF MEMS switches and circuits
 - · Sub-system example: passive electrically steerable array
- · Heterogeneous integration
 - Active and RF MEMS components are stacked and connected with cost effective interconnection and wafer level techniques
 - · Can be very cost and size effective



VTT TECHNICAL RESEARCH CENTRE OF FINLAN

Future Directions

- · Monolithic integration
 - · RFIC/MMIC are integrated together with RF MEMS switches and circuits
 - · Maximizes the integration level at the same chip
 - · Lower number of interconnects
 - · On-going EU-funded project MEMS-4-MMIC













VIII